



CERTIFICATE OF MAILING (37 CFR 1.8a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: BOX NON-FEE AMENDMENT, Commissioner for Patents, Washington, D.C. 20231.

Date: 12/11/02

JANET FARR  
(Type or print name of person mailing paper)

Janet Farr  
(Signature of person mailing paper)

**YAMAP0347USB**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Uriu, et al.

Art Unit: ~~2832~~

Serial No: 09/525,247

Examiner: T. Nguyen

Filed: March 15, 2000

For: INDUCTOR AND METHOD FOR PRODUCING THE SAME

**RECEIVED**

**DEC 17 2002**

Technology Center 2600

REPLY TO OFFICE ACTION MAILED SEPTEMBER 18, 2002

BOX NON-FEE AMENDMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

This Reply is filed as a complete response to the Office Action mailed September 18, 2002, for which a three month time for reply was set. Accordingly, Applicants' Reply is timely filed. Applicants request entry of the present Reply, and reconsideration of the present application based thereon.

**RECEIVED**  
**DEC 20 2002**  
TECHNOLOGY CENTER 2800

3A



RECEIVED

DEC 17 2002

AMENDMENT

Please amend claim 8 to read as follows:

Technology Center 2600

8. (Three times Amended) A lamination ceramic chip inductor, comprising at least one fine, continuous conductive pattern, the at least one fine, continuous conductive pattern having a thickness of 10  $\mu$ m or more and a width to thickness ratio from 1 to less than 5.

A version of the above amended claim marked to indicate the specific amendments may be found in the attached Appendix, in accordance with 37 CFR 1.125(c)(1).

REMARKS

Claims 8-15 and 21-50 are pending in the present application. Applicants submit herewith a second Declaration under 37 CFR §1.132. Applicants' undersigned attorney notes for the record that the date inscribed on the first page of the Declaration is the date on which the Declaration is submitted, i.e., the filing date of the present Reply, whereas the Declaration itself was executed and dated by the Declarant, Mr. Uriu, on December 2, 2002, as shown on the final page of the present Declaration.

Applicants submit herewith a certified translation of the Japanese priority application, Japanese Patent Application No. 6-217150, filed on 12 September, 1994 in the name of MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. The translation is certified by Mr. Takeshi Oshio, a Japanese Patent Attorney personally known to the undersigned attorney.

Rejection of Claims 8-15 and 21-50 as Indefinite

Claims 8-15 and 21-50 stand rejected as indefinite. Applicants respectfully traverse these rejections for the following reasons.

Claim 8 and therefore claims 9-11, stand rejected because there is no antecedent basis for "the at least one fine, continuous conductive pattern". Applicants have amended